680 MOD 1 MHz CPU CLOCK

(1) REMOVE SS-2 from socket
jumper to SS-13

(2) Remove SS-6 from socket
jumper to SS-5

(3) Remove SS-11 from socket
jumper TO SS-5

(4) CONNECT I.C. TT-2 TO TT-11
(CLK of A TO GND)

(5) ~~CONNECT~~ BRIDGE TT-6 TO TT-7

(6) REMOVE C11 (.001uf)

NOTE: FAST ACCESS PROM REQUIRED.

PROM REQ
CYCLE 1us
DATA MUST BE VALID 100 ns
BEFORE Ø2 FALLS
CS + ADDR DELAY ≤ 130 ns

1us − 230 770 ns MAX
MOSTEK 3702-1 — 500 − 600 ns MAX ACC from

6806-BSM 16K 1MHZ CPU CLOCK MOD

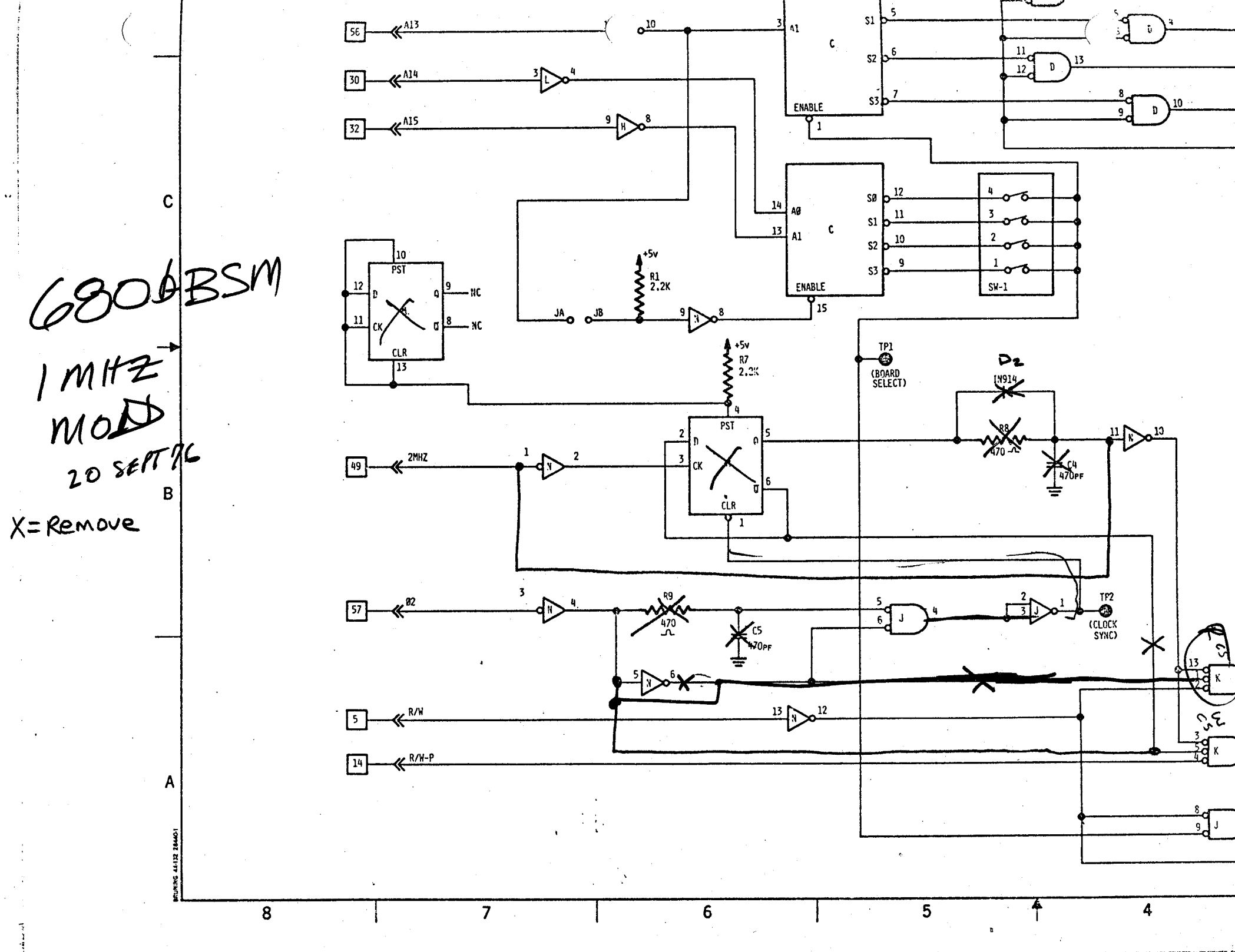
(1) Remove D2, R8, C4, R9, C5 AND I.C. M

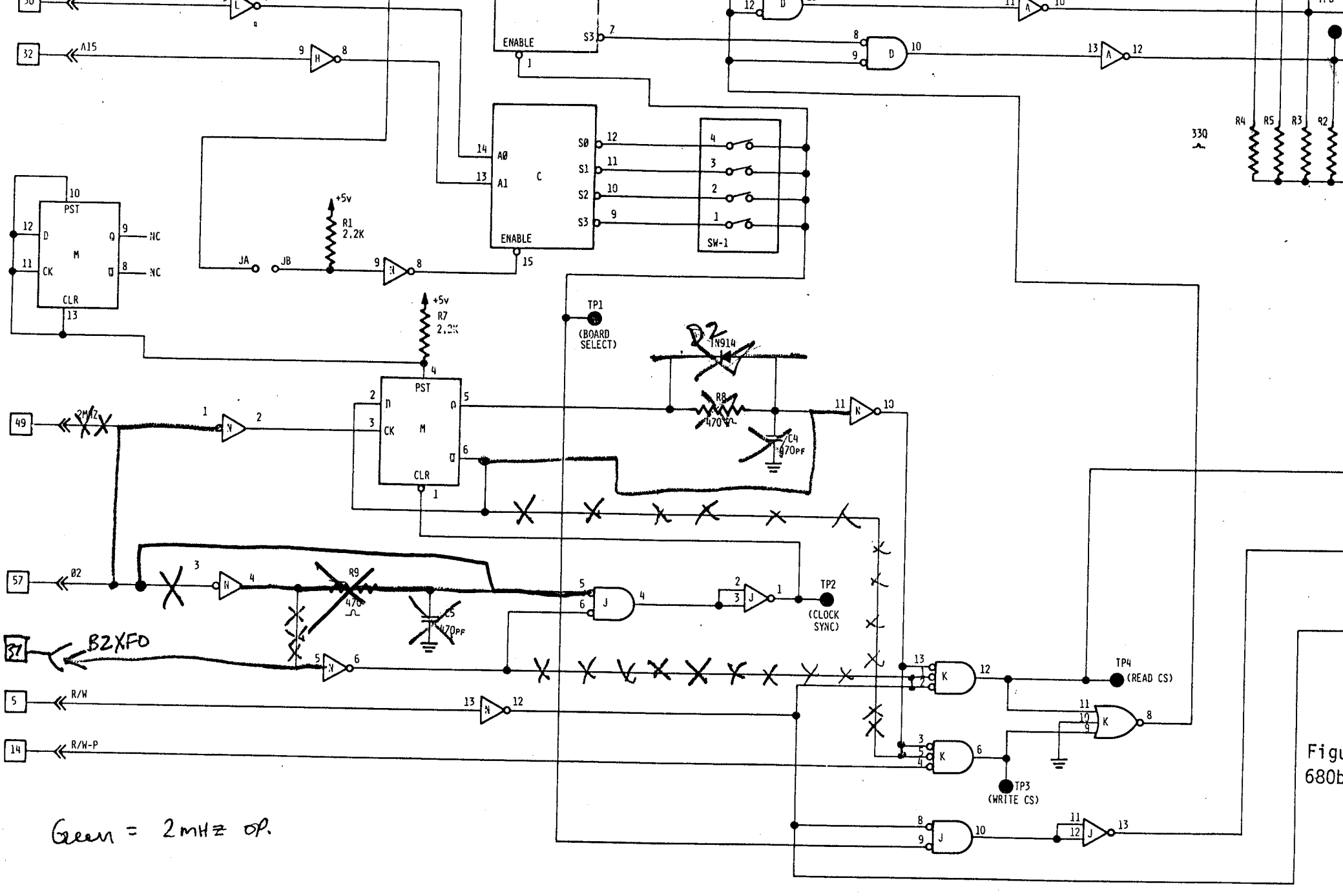
(2) JUMPER N-11 TO N-1 ON BOTTOM OF BOARD (2MHZ BUS SIGNAL)

~~(3) BRIDGE M-1 TO M-2 ON BOTTOM OF BOARD ($\overline{\phi 2}$ FOR WCS)~~

~~(4) BEND J-5 UP AND OVER TO SOLDER J-6 TO J5~~

(3) JUMPER K-5 TO N-5 ($\overline{\phi 2}$ FOR WCS)


6800BSM
1 MHZ
MOD
20 SEPT 76
X=Remove
A13
A14
A15
2MHZ
Ø2
R/W
R/W-P
+5v
R1
2.2K
R7
2.2K
R8
470
R9
470
C4
470PF
C5
470PF
D2
IN914
TP1
(BOARD SELECT)
TP2
(CLOCK SYNC)
SW-1
PST
CLR
ENABLE
NC
BRUNING 44132 28401



Figu
680b

7 6 5 4 3 2

SALES MGR

DICK BURGER — METROPOLITAN [redacted] CIRCUITS

714-549-2204

4K & 16K CLOSE TOLERANCES

PIN HOLES IN ARTWORK & SCRATCHES

CAN WE GO THERE?

JOHN DYLAN - [redacted] ENG. MGR.